

Organizing Committee

Chief Patron

President and Office Bearers,
Kalpataru Vidya Samsthe,
Tiptur-572201.

Patron

Dr. Nandeeshaiha, M.E., Ph.D. (I.I.T - B)
Principal,
KIT, Tiptur.

Convener

Prof. G S Yogananda
Head of the Department of E&CE
KIT, Tiptur.

Project Coordinator

Dr. S V Rajashekararadhya, M.E., Ph.D.
Professor, Department of E&CE,
KIT, Tiptur.

Registration

The participants can download Registration Form available on the KIT website (<http://www.kittiptur.ac.in/aictefdp/index.php>) or otherwise can use the registration form available overleaf.

All shortlisted candidates are required to confirm their participation by sending a hard copy Registration Form and Demand Draft of Rs.500/- in favour of **"The Principal, KIT, Tiptur"**.

The above amount will be refunded to the participant after valedictory function.

Completed registration form should be sent to us through E-mail: aictefdp@kittiptur.ac.in on or before **1st July, 2019**.

The number of participants will be limited. The selected applicants will be intimated by E-mail on or before **3rd July, 2019**.

Note: The certificate will be issued to those participants who have attended the programme and have qualified in the test conducted by PEC at the end of the program.

About the College

Kalpataru Institute of Technology, Tiptur is located at Bengaluru-Honnar, National Highway NH-206, at distance of 144km from Bengaluru well connected by Road & Railway transport.

Kalpataru Institute of Technology, premier centre for technical education was established in the year 1986 and celebrated its Silver Jubilee in the year 2012.

The campus is in a sprawling area of 26 acres with well laid buildings, state of art laboratories with latest equipments and softwares, smart class rooms, ultra-modern sports facilities, better hostel facilities for both boys & girls inside the campus and lush green garden.

The college is affiliated to Visvesvaraya Technological University, Belagavi and approved by AICTE, New Delhi.

About the Department



The Vision of the department is to contribute towards the development of Technology in the field of Electronics & Communication so that mankind can have more benefits from our works and thoughts.

The department provides an active research-based curriculum to provide the fundamental and technical education.

The alumni of the department are placed in top positions of many companies around the globe. The alumni are contributing to the department in terms of technological transfer and training.

The department strives to produce excellent professionals through comprehensive educational programmes, promoting students with managerial skills to become competitive professionals, carrying out researches in collaborative with industries and adaptive to ever-changing technologies.

The department has student intake of 120, faculty strength of 26 and attracts the brightest students.

Two Week AICTE Sponsored Faculty Development Programme



On

"Machine Learning, Artificial Intelligence in Image Processing using MATLAB and Xilinx System generator for DSP"



From



22nd July to 3rd August, 2019



Organized by



Department of Electronics and Communication Engineering

and

In association (Technical) with



Enabling Excellence

CoreEL Technologies (I) Pvt Ltd

KALPATARU INSTITUTE OF TECHNOLOGY

Approved by AICTE, New Delhi, Affiliated to VTU Belagavi,
N.H 206, Tiptur-572201, Karnataka, India.

Website: www.kittiptur.ac.in

About the Workshop

Machine Learning (ML) and Artificial Intelligence (AI) is a hot topic in the tech industry. Perhaps more than our daily lives AI is impacting the business world more. There was about \$9.3 Billion in venture capital invested in AI start-ups in 2018, a 300% increase than a year before.

AI is the science and engineering of making intelligent machines, especially intelligent computer programs. Artificial Intelligence is related to the similar task of using computers to understand human intelligence, but AI does not have to confine itself to methods that are biologically observable.

ML is a subset of AI. ML is a science of designing and applying algorithms that are able to learn things from past cases. If some behaviour exists in past, then you may predict if or it can happen again. Means if there are no past cases then there is no prediction.

Objectives

Objectives of the training are:

- To familiarize with AI and explore ML libraries and the fundamentals of image processing.
- To familiarize Xilinx System Generator for DSP that enables designers to develop high-performance DSP systems
- To study the recent engineering problems in data processing and image processing.
- To evolve effective optimized algorithm on image processing problems.
- To develop new methods and algorithms for training, effective teaching and research.
- To implement image processing algorithms.
- To discuss the various technique and applications in image processing, speech processing, video processing, etc.
- To train faculties for exploring research opportunities and carryout innovative interpretations.
- To equip faculties with skills and knowledge in the field of computer vision, speech processing, image processing and video processing which are essential for inculcating learning values in students, guiding and monitoring their progress towards their career.

CoreEL Technologies (I) Pvt Ltd

CoreEL Technologies is a technology company with businesses spread across design services and product development, distribution and training. Head Quartered in Bangalore, India, CoreEL is a leading provider of VLSI and Embedded System design services and Intellectual Property. Since its inception in 1999, CoreEL Technologies a privately held corporation has always strived to deliver quality solutions and support in all the business areas that it serves. Our services offerings include distribution of silicon solutions, EDA tools, COTS products, Engineering Services (Turn Key Systems Design, Turn Key FPGA Design and High-Speed PCB Design) manufacturing. These services are offered to our broad customer base comprising Defence and Aerospace, Telecommunication and Networking, Homeland Security, Broadcast Video and Education segments.

Eligibility

This programme is open to the faculty members of ECE/TCE/CSE/ISE/IT/ML/BME departments of AICTE approved Engineering Colleges, Polytechnic Colleges and also Engineers from industry/research organizations. The outstation participants are eligible for TA by the shortest route as per norms.

Important Dates

Last date for receiving scanned registration form through e-mail (aictefdp@kittiptur.ac.in) is **1st July, 2019**.

Selected participants will be intimated through E-mail, on or before **3rd July, 2019**.

Last date for receiving Demand Draft from the confirmed participants through post is **10th July, 2019**, to the address of the event coordinator mentioned below.

Address for Communication

Event Coordinator [Technical]

Prof. Shambhavi S

Assistant Professor, Department of E&CE

Kalpataru Institute of Technology,

N.H 206, Tiptur-572201, Karnataka.

Ph.No: 9844767142

E-mail: aictefdp@kittiptur.ac.in

Registration Form

(Copies available at <http://www.kittiptur.ac.in/aictefdp/index.php>)

AICTE Sponsored

Two Week Faculty Development Programme

On

“Machine Learning, Artificial Intelligence in Image Processing using MATLAB and Xilinx System generator for DSP”

22nd July to 3rd August, 2019

1. Name:
2. Designation:
3. Institution:
4. Address for Communication:
.....
.....
.....PinCode:.....
5. Mobile :
6. E-mail:
7. Highest Qualification:
8. Specialization:
9. Experience:.....
10. Whether accommodation needed: Yes /No:

Applicant's Signature

Sponsorship

Mr./Ms./Dr. : is a faculty of our institution and is hereby sponsored. He/She will be permitted to attend the programme, if selected. He/She will abide by the rules and regulations of the host Institute.

Signature of the Head of the Institution
(With seal)