

FACULTY PROFILE - 22



1. **Name** : LOHITH MS
 2. **Date of Birth** : 08.03.1987
 3. **Highest Qualifications** : M.Tech. (Digital Electronics)
 4. **Academic Performance** :

(High School to Highest Qualifications)

Qualifications	Board / IIT / University	Institute and place where studied	Year of passing	Class & % marks
SSLC	KSEE Board	MJC, Ammasandra	2002	SC/57
PUC / 10+2	KDPU Education	KSC, Tiptur	2004	FC/61
UG Degree	VTU, Belagum	KIT, Tiptur,	2009	SC/58
PG Degree	SS Deemed Uni.	SSIT, Tumkur	2011	CGPA/9

5. **Date of Joining** : 26.12.2011
 6. **Status as on date of Joining** : Lecturer
 7. **Salary as on date of Joining** : Rs.22,000/-
 8. **Present Status** : Assistant Professor
 9. **Salary as on date** : Rs.34,000/-
 10. **Number of promotions since date of joining** : Nil
 11. **Achievements since date of joining:**
 Faculty Development / QIP : 08
 Research & Development : -
 No. of Publication (List) : -
 Teaching Award : -
 Books / Monograph (List) : -
 Conference Seminars (List) : 03
 Extra curricular activities* : --
 Administrative Work : Placement Coordinator

FD-Faculty Development, R&D-Research and Development

Publication Date	Name, title of the Paper, N/I Journal, pages, year, also list here the Books and Monographs	Prizes / Awards
22/10/2018	LOHITH M S, Multimodal Biometrics for Person Identification using Ear and Palmprint Features, IETE Journal of Research, I, 8, 2018.	-
20/12/2021	, Multimodal Biometric Person Authentication using Face, Ear and Periocular region based on Convolution Neural Networks, International Journal of Image and Graphics, I, 14, 2021	
Conference / Seminar/W/s	Name, title of the paper, Conference / Seminar, venue and date	Prizes / Awards

12/11/2020	LOHITH M S, CAF Technique in Position Recognition for Wireless Applications , IEEE Conference, 2020	
2011-12	AICTE Sponsored, Short-term Course on “FPGA Proto-typing using Verilog,” IIT, Karaghpur, June 25-30, 2012.	-
Jan 2013	CLHRD Sponsored workshop on “Feel Teacher”, KIT, Tiptur, Jan 28 th to 30 th 2013	
July 2013	AICTE Sponsored, Short-term Course on “Medical Signals and Image Processing,” IIT, Roorkee, July 15-19, 2013	
June 2013	ISTE Sponsored National workshop on “Analog Electronics” at SJCE, Mysore , June 4 th to 14 th 2013	
March 2014	National Workshop on “Nuvoton ARM Cortex-M0 and its application”, VTU CPGS, Bangalore, March 18 th to 19 th 2014.	
April 2014	National Workshop on “Latex-A Tutorial Perspective”, JN NCE, Shimoga, April 25 th to 26 th 2014	
April 2014	AICTE Sponsored, Short-term Course on “Analog and Digital VLSI Design using Cadence Tools”, RIT, Hassan, April 28 th to 30 th .	
	AICTE Sponsored, workshop on “Signals and Systems”, MIT, Manipal.	
2010-11	“Advanced Location Estimation”, 2 nd International Conference on Simulation, Modeling & Analysis, COSMA-2011, Dec 14-16, 2011, Amrita School of Engg, Coimbatore	-
	IRDO, “Location Estimation Using CAF-Map Method”, International Conference, June 2011	-
May 2015	“Biometric System for Person Authentication-A Review” ICNIC-2015	
	Participation in Co and extracurricular activities (member of respective committees / office bearer / Group leader etc.)	Prizes / Awards
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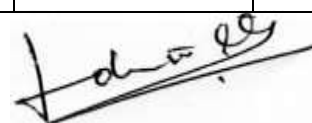
12. Experience / Service before Joining:

Sl. No.	Name & Address of Organization / Institution	Post Held / Designation	Duration of service		Experience Y/M			
			From	To	T	R	Ind.	Total
01	ISRO	Project Trainee	August 2010	June 2011				10

T – Teaching, R – Research, Ind. - Industry

13. Memberships of Professional Bodies

Sl. No.	Name & Address of Organization / Institution	Date & Year of Registration	Membership Number	
1	ISTE		--	--



(LOHITH M S)