

FACULTY PROFILE - 09

1. **Name** : **PRATHIBHA.C**
2. **Date of Birth** : **24.12.1978**
3. **Highest Qualifications** : **M.Tech.(Computer Science & Engineering)**
4. **Academic Performance** :
(High School to Highest Qualifications)



Qualifications	Board / IIT/University	Institute and place where studied	Year of passing	Class & % marks
SSLC	KSEE Board	Tagore High School, Tiptur	1994	FCD/84
PUC / 10+2	KDPU Education	SVP Junior College, Tiptur	1996	FCD/81
B.E.	Bengaluru University	Kalpataru Institute of Technology, Tiptur	2000	FC/62
M.Tech.	VTU Belgaum	Malnad College of Engg, Hassan	2013	FCD/77

5. **Date of Joining** : **01.09.2007**
6. **Status as on date of Joining** : **Lecturer**
7. **Salary as on date of Joining** : **Rs.12,000/-**
8. **Present Status** : **Assistant Professor**
9. **Salary as on date** : **Rs.35,641/-**
10. **Number of promotions since date of joining** : **--**

11. Achievements since date of joining:

Faculty Development / QIP : 06 / Pursued M.Tech (2010-13)
Research & Development : -
No. of Publication (List) : -
Teaching Award : -
Books / Monograph (List) : -
Conference Seminars (List) : 01
Extra curricular activities* : -
Administrative Work : Nil

FD-Faculty Development, R&D-Research and Development

Publication Date	Name, title of the Paper, N/I Journal, pages, year, also list here the Books and Monographs	Prizes / Awards
CAY 21-22	-	
CAYm1 20-21	-	--
CAYm2 19-20	-	--
CAVm3 18-19	-	--
CAVm4 17-18	-	--
CAVm5 16-17	-	--
CAVm6 15-16	-	--
CAVm7 14-15	-	--
CAVm8 13-14	-	--
CAVm9 12-13	-	--
CAVm10 11-12	-	--
CAVm11 10-11	-	--

Conference / Seminar/W/s	Name, title of the paper, Conference / Seminar, venue and date					Prizes / Awards
	Sl No	Type	Title	Place	Date	
CAY 2021-22	01	Online One week FDP	Introduction to Microchip FPGA	organized by Department of Electronics & Communication Engineering, Bapuji Institute of Engineering and Technology, Davangere in association with Tecnomac Components Pvt. Ltd. Bengaluru, ISTE Faculty Chapter BIET and IETE Shivamogga Center.	19th to 23rd July 2021	
	02	One week FDP online	“Digital VLSI Design and Verification”	organized by Department of ECE, Bangalore Institute of Technology in association with Entuple Technologies and IEEE-BIT CAS from	23rd to 27th August 2021	
	03	One week FDP online	Current Era- A Technology driven Digital Trasformation in Industry”	Organized by Department of ISE , SJB Institute of Technology, Kengeri, Bangalore	30/08/2021 to 04/09/2021	

CAYm1 2020-21	01	One week FDP (Online)	Recent Trends in Data Science	Theem College of Engineering, Approved by University of Mumbai, MSBTE & In Association with CSI & IETE(Students Chapter)	01 to 5 th July 2020	--
	02	Online Webinar	“Course Outcomes Based on OBE”	Department of Telecommunication Engg, Dr. Ambedkar Institute of Technology, Bengaluru	27 th July 2020	
	03	Online Webinar	Importance of having a Vision and Self Belief	Department of ISE, SJBIT, Kengari, Bangaluru	15 th August 2020	
	04	National Webinar	"Cyber Security Concerns in E-learning Education [Intellectual Property Rights in E-learning]"	Department of Computer Science and Engineering, Kalpataru Institute of Technology, Tiptur	on 28th June 2021.	
CAYm2 19-20	01	Online Webinar	“Non Orthogonal Multiple Access for 5G and beyond”	Department of Electronics and communication Engineering, Global Academy of Technology, Bengaluru	on 10 th June 2020	
	02	Online Webinar	“Advances in IOT and its Applications”	organized by Department of Electronics & Communication Engineering, at Dr. Ambedkar Institute of Technology, Bengaluru.	on 10 th June 2020	
	03	Online Webinar	“Cyber Security“	Department of Computer Science & Engineering at Dr. Ambedkar Institute of Technology, Bengaluru.	held on 11.06.2020	
	04	Online Webinar	“RF DOMAIN USING ANSYS HFSS” presented by Mr. R. Shashi Kumar, Application Engineer, Entuple Technologies pvt. Ltd., Bangalore	organized by Department of Electronics and Communication Engineering , AMRITA College of Engg & Technology	on 12th June, 2020	
	05	Online Webinar	“Career Opportunities in VLSI Industry”	organized by the Department of Electronics and Communication Engineering. Sapthagiri College of Engineering, Bangalore	held on 12th June 2020	
	06	Online Webinar	“A REAL TIME AUDIO SYSTEM DESIGN FOR COMMERCIAL MARKET” presented by Mr. S.V. Sreeraj, Director, EmCog Solutions, Chennai.,	organized by Department of Electronics and Communication Engineering , AMRITA College of Engg & Technology	on 13th June, 2020	
	07	Online Webinar	“Doing Research: Challenges, Dos and Don'ts” under IQAC Cell	organized by ECE Research Centre under VTU, Belagavi and GSSS Institute of Engineering & Technology for Women, Mysuru	on June 16, 2020	

	08	National level live Webinar	'Intellectual Property Rights & Indian Patent Filing Procedure'	Proudhadevaraya Institute of Technology, T. B. Dam, Hosapete	on 19th June 2020.	
	09	International online Webinar	"Agile Development Life Cycle"	Organized by the Department of Computer Science, K.S.R College of Arts and Science For Women, Tiruchengode	on 19th June 2020	
	10	Online Webinar	"Power for CMOS VLSI circuits"	IEEE Circuits and Systems (CAS) Society Bangalore Chapter in Association with IEEE Bangalore Section	20th June, 2020	
	11	Online Webinar	"Low-Power Design Techniques for Digital Circuits"	IEEE Circuits and Systems (CAS) Society Bangalore Chapter in Association with IEEE Bangalore Section	26th June, 2020	
	12	Online Webinar	"VLSI Design for Artificial Intelligence and Machine Learning"	conducted by the Department of Electronics and Communication Engineering, Global Academy of Technology, Bengaluru	on 29th June 2020.	
CAYm3 18-19	01	Workshop	Learning Effective Document Writing Using Latex	Conducted by Electronics & Communication by Kalpataru Institute of Technology, Tiptur	On April 15, 2019	
	02	National Conference	Paper presentation on Smart ration card system for Public Distribution Using RFID and GSM	National Conference on "Recent Trends in Electronics, Communication & Computers" NCRTECC Conducted by Electronics & Communication by Kalpataru Institute of Technology, Tiptur	8 th May 2019	
CAYm4 17-18		FDP	Advances in Image Processing & Computer Vision Application using Python and Open CV	Conducted by Electronics & Communication by Kalpataru Institute of Technology, Tiptur	11 th to 23 rd December 2017	
CAYm5 16-17		--	--			
CAYm6 15-16		--	--			
CAYm7 14-15		--	--			
CAYm8 13-14		Workshop	on "Analog and Digital VLSI Design Using Cadence Tools"	Organized by RIT , Hassan	on 28 th to 30 th April, 2014	
CAYm11 09-10	01	FDP	on VLSI Lab	Organized by SJBIT, Bangalore,	July 3,2009	
	02	Workshop	on CMOS VLSI & ASIC Design using Microwind Tool,	SIT, Tumkur	Aug 27-28, 2009	

Co & Extra-curricular	Participation in Co and extracurricular activities (member of respective committees / office bearer / Group leader etc.)	Prizes / Awards
CAY 2020-21	Nil	--
CAY 2019-20	Nil	--
CAY 2018-19	Nil	--

12. Experience / Service before Joining:

Sl. No.	Name & Address of Organization / Institution	Post Held / Designation	Duration of service		Experience Y/M			
			From	To	T	R	Ind.	Total
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T – Teaching, R – Research, Ind. – Industry

13. Memberships of Professional Bodies

Sl. No.	Name & Address of Organization / Institution	Date & Year of Registration	Membership Number	
1	LMISTE	31.03.2010	LM71191	

Prathiba

(PRATHIBHA C)