

FACULTY PROFILE



NAME: SHILPA C N

Designation: Assistant Professor

Department of Electronics & Communication Engineering

Date of Joining:	20-09-2023
Length of Professional Experience:	2 years

Personal and Contact Details

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Academic Details

M. Tech (Digital Electronics), PESITM Shivamogga, VTU, Belagavi, Karnataka, 2016

B. E (E&CE), Jnana Vikas Institute of Technology, Bidadi Bangalore, VTU, Belagavi, Karnataka, 2012

Research and Article Publications

Ms. Shilpa C N, Mr. Kunjan D. Shinde, Mr. Nithin H V, "Modeling and Comparative Analysis of Logic Gates for Adder and Multiplier Applications -A VLSI based approach"

IOSR Journal of VLSI and Signal Processing (IOSR-JVSP), Volume 6, Issue 3, I (May-Jun 2016), PP67-72 e-ISSN: 2319-4200, p-ISSN No.: 2319-4197. <http://www.iosrjournals.org/iosr-jvlsi/papers/vol6-issue3/Version-1/K0603016772.pdf>

"Impact of VLSI Design Techniques on Implementation of Parallel Prefix Adders" © Springer Nature Singapore Pte Ltd. 2018 I. Zelinka et al. (Eds.): ICSCS 2018, CCIS 837, pp. 473-482, 2018. https://doi.org/10.1007/978-981-13-1936-5_50

Conference on "Design, Implementation and Comparative Analysis of Kogge Stone Adder using CMOS and GDI design: A VLSI Based Approach"

published in: 2016 8th International Conference on Computational Intelligence and Communication Networks (CICN). Date on 23-25 Dec 2016, 978-1-5090-1144-5/16 \$31.00 © 2016 IEEE DOI 10.1109/CICN.2016.117.

<https://ieeexplore.ieee.org/document/8082709>

Achievements

FACULTY DEVELOPMENT PROGRAMS

6 Days ATAL FDP on “Revolutionizing Electric Vehicle Powertrains: The Role of Wide-Bandgap Semiconductors in Overcoming Challenges and Shaping Future Trends” at DR.Mahalingam College of Engineering and Technology from 17/02/2025 to 22/02/2025.

6 Days ATAL FDP on “Smart Communication In IOT: Security & Future Applications and Possibilities” at St.Joseph College of Engineering from 24/02/2025 to 01/03/2025.

NPTEL COURSES

8 Week NPTEL SWAYAM online course on “Digital Circuits”, JUL-OCT 2024



(SHILPA C N)